

	Type	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	29390	test\$3 with (PCB or (printed adi circuit adj board))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:25	
2	BRS	7	test\$3 with (PCB or (printed adi circuit adj board)) and substrate\$1 with optical with transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:31	
3	BRS	6198	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:25	
4	BRS	1678	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:26	
5	BRS	353	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/24 17:26	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
6	BRS	249	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
7	BRS	8	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:43	
8	BRS	4	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:28	

Type	Hits	Search Text	DBs	Time Stamp	Comments
9 BRS	4	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
10 BRS	50	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
11 BRS	15	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:45	

Type	Hits	Search Text	DBs	Time Stamp	Comments
12	BRS 0	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (sub- Component\$1) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:50	
13	BRS 4	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 with (subcomponent\$1 or (sub- Component\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:06	

Type	Hits	Search Text	DBS	Time Stamp	Comments
14	22	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:43	
15	20	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:12	

Type	Hits	Search Text	DBs	Time Stamp	Comments
16	BRS 57	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	
17	BRS 26	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) and upper with surface\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
18	BRS	16	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) and trace\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:14	
19	BRS	13	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) and trace\$1 and expose\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:18	

Type	Hits	Search Text	DBS	Time Stamp	Comments
20	1	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and trace\$1 and trace\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	
21	11	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	

Type	Hits	Search Text	DBs	Time Stamp	Comments
22	11	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:36	
23	11	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:37	

Type	Hits	Search Text	DBs	Time Stamp	Comments
24	BRS 25	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:38	
25	BRS 745	702/117.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
26	BRS 521	209/571,573.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
27	BRS 1234	324/539,755.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:39	
28	BRS 206	710/64.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 09:47	

Type	Hits	Search Text	DBs	Time Stamp	Comments
29 BRS	9399	324/539, 754, 755, 757, 758, 765.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:40	
30 BRS	6	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and (remov\$4 or insert\$4 or attach\$3) with socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:47	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
31	BRS	10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:48	
32	BRS	10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (component\$1) and (remov\$4 or insert\$4 or attach\$3) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:56	

Type	Hits	Search Text	DBs	Time Stamp	Comments
33	BRS 10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and socket\$1 and socket\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:00	
34	BRS 10	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and socket\$1 and socket\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	

Type	Hits	Search Text	DBs	Time Stamp	Comments
35	17	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and pin\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	
36	3	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (component\$1) and base with (insert\$4 or attach\$4 or receiv\$3) with component\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	

Type	Hits	Search Text	DBs	Time Stamp	Comments
37	17	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receive\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and base with (insert\$4 or attach\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	
38	12	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receive\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and base with (insert\$4 or attach\$4) and pin\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	

Type	Hits	Search Text	DBs	Time Stamp	Comments
39	BRS 151	439/39.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:32	
40	BRS 8	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and electric\$4 with (communication\$1 or interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	

Type	Hits	Search Text	DBS	Time Stamp	Comments
41	BRS 4	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with recv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and electric\$4 with interface\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	
42	BRS 21	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with recv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
43	BRS	21	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	
44	BRS	233	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:37	
45	BRS	44	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppose\$3 with end\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
46	BRS	32	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
47	BRS	31	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
48	BRS	2	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
49	BRS	4	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
50	BRS	0	computer\$4 same test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:39	
51	BRS	3	computer\$4 and test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:41	
52	BRS	146	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:50	
53	BRS	6	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:56	

Type	Hits	Search Text	DBs	Time Stamp	Comments
54 BRS	6	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:56	
55 BRS	262	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	
56 BRS	97	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	

Type	Hits	Search Text	DBs	Time Stamp	Comments
57 BRS	82	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and transceiver\$1 or (transceiver\$4 or receiv\$4 or transmit\$4 or receivev\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:05	
58 BRS	0	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 with head\$2 and computer\$4 with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:06	
59 BRS	7	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 with head\$2 and computer\$4 and port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
60 BRS	591	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) with electric\$4 with connect\$4 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
61	BRS	23	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) with electric\$4 with connect\$4 and computer\$4 with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
62	BRS	12	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) with electric\$4 with connect\$4 and computer\$4 with port\$1 and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:08	
63	BRS	0	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) with electric\$4 with connect\$4 and clamp\$1 with head\$3 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
64	BRS	1	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) with electric\$4 with connect\$4 and clamp\$1 with head\$3 same arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
65	BRS	31308	test\$3 with (PCB or (printed adi circuit adj board)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:25	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
66	BRS	6802	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:25	
67	BRS	1846	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
68	BRS	398	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
69	BRS	286	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	
70	BRS	9	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:26	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
71	BRS	5	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:28	
72	BRS	5	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
73	BRS	9	test\$3 with (PCB or (printed adi circuit adj board)) and substrate\$1 with optical with transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
74	BRS	51	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:31	
75	BRS	16	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base and (subcomponent\$1 or (sub- component\$1)) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:45	

Type	Hits	Search Text	DBs	Time Stamp	Comments
76 BRS	1	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1) and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/24 17:50	
77 BRS	5	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 with (subcomponent\$1 or (sub- component\$1) or component\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:06	

Type	Hits	Search Text	DBs	Time Stamp	Comments
78	BRS 25	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 adj base and secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 08:43	
79	BRS 21	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:12	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
80	BRS	60	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	
81	BRS	29	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1)) and upper with surface\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:13	

Type	Hits	Search Text	DBS	Time Stamp	Comments
82	BRS 18	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) and trace\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:14	
83	BRS 15	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or transmit\$4 or receivev\$4 or interface or coupon) with board\$1 and (subcomponent\$1 or (sub- component\$1) or component\$1) and trace\$1 and expose\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:18	

Type	Hits	Search Text	DBS	Time Stamp	Comments
84	2	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and trace\$1 and trace\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:21	
85	13	test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:28	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
86	BRS	13	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:36	
87	BRS	13	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1) and electric\$4 with connect\$4 and connect\$3 with base\$1 with secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:37	

Type	Hits	Search Text	DBs	Time Stamp	Comments
88 BRS	58	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:38	
89 BRS	11292	702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
90 BRS	1223	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.) and test\$3 with (PCB or (printed adj circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
91	BRS	325	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
92	BRS	42	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
93	BRS	0	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.) and test\$3 with (PCB or (printed adj circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	
94	BRS	219	710/64.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:47	

Type	Hits	Search Text	DBs	Time Stamp	Comments
95	6	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and (remov\$4 or insert\$4 or attach\$3) with socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:47	
96	11	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:48	

Type	Hits	Search Text	DBS	Time Stamp	Comments
97	11	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and (remov\$4 or insert\$4 or attach\$3) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 09:57	
98	14	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s.) and test\$3 with (PCB or (printed adj circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB	2005/10/25 10:34	

Type	Hits	Search Text	DBS	Time Stamp	Comments
99	11	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and socket\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:00	
100	11	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (subcomponent\$1) and socket	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
101	BRS	50	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and pin\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	
102	BRS	3	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and base with (insert\$4 or attach\$4 or receiv\$3) with component\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:29	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
103	BRS	50	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (component\$1) and base with (insert\$4 or attach\$4) (insert\$4 or attach\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	
104	BRS	45	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or (component\$1) and base with (insert\$4 or attach\$4) and pin\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:30	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
105	BRS	155	439/39.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:32	
106	BRS	11447	702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
107	BRS	1225	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
108	BRS	326	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.) and test\$3 with (PCB or (printed adj circuit adj board)) and electric\$4 with connect\$4 and connect\$3 with base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:33	
109	BRS	43	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.) and test\$3 with (PCB or (printed adj circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
110	BRS	0	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base and (subcomponent\$1 or (sub- component\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:34	
111	BRS	15	(702/117.cc1s. or 209/571,573.cc1s. or 710/64.cc1s. or 324/539,754,755,757,758,76 5.cc1s. or 439/39.cc1s.) and test\$3 with (PCB or (printed adi circuit adj board)) and electric\$4 with connection\$1 and connect\$3 adj base	US-PGPUB	2005/10/25 10:34	

Type	Hits	Search Text	DBS	Time Stamp	Comments
112 BRS	40	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and electric\$4 with (communication\$1 or interface)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	
113 BRS	36	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and electric\$4 with interface\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:35	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
114	BRS	54	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	
115	BRS	54	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and connect\$3 with base\$1 same secur\$3 and (transceiver\$1 or (transmit\$4 with receiv\$4)) and (subcomponent\$1 or (sub- component\$1) or component\$1) and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:36	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
116	BRS	272	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:37	
117	BRS	48	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
118	BRS	35	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
119	BRS	34	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
120	BRS	3	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and transceiver\$1 and conduct\$5 and oppos\$3 with end\$1 and computer\$4 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
121	BRS	5	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:38	
122	BRS	1	computer\$4 same test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and transceiver\$1 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:39	
123	BRS	152	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
124	BRS	7	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
125	BRS	7	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and receptacle\$1 with oppos\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
126	BRS	282	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
127	BRS	102	test\$3 with (PCB or (printed adi circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 10:57	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
128	BRS	87	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or (transmit\$4 or receiv\$4 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
129	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or (transmit\$4 or receiv\$4 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
130	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or (transmit\$4 or receiv\$4 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
131	BRS	77	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
132	BRS	5	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 and receptacle\$1 with opposite\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:01	
133	BRS	4	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and receptacle\$1 with opposite\$3 with end\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
134	BRS	159	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 and clamp\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	

	Type	Hits	Search Text	DBS	Time Stamp	Comments
135	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:02	
136	BRS	33	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and computer\$4 and secur\$3 with clamp\$1 and (transceiver\$1 or transmit\$4 or receiv\$4 or interface or coupon)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:05	
137	BRS	1	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1)) and electric\$4 with connect\$4 and clamp\$1 with head\$2 and computer\$4 with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:06	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
138	BRS	4	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) and electric\$4 with connect\$4 and clamp\$1 with head\$2 and computer\$4 and port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
139	BRS	333	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and computer\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
140	BRS	11	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and computer\$4 with port\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:07	
141	BRS	7	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and computer\$4 with port\$1 and secur\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:08	
142	BRS	1	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and clamp\$1 with head\$3 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
143	BRS	1	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and clamp\$1 with head\$3 with arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:09	
144	BRS	2	test\$3 with (PCB or (printed adj circuit\$1 adj board\$1) with electric\$4 with connect\$4 and clamp\$1 with head\$3 same arm\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 11:42	
145	BRS	0	mills-randell.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:22	
146	BRS	6957	mills.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:22	
147	BRS	47	mills.in. and mills near randell	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:22	
148	BRS	34	mills.in. and mills near randell and hydro\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2005/10/25 12:23	

	Type	Hits	Search Text	DBs	Time Stamp	Comments
149	BRS	4	mill\$.in. and mills near randell and hydro\$5 and maxwellian	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM TDB	2005/10/25 12:23	

	U	1	Document ID	Issue Date	Page s	Title	Current OR	Current XRef	Retrieva 1 Classif
1			US 2005005219 3 A1	20050310 8		Notched electrical test probe tip	324/754		324/754
2	X		US 2005000164 0 A1	20050106 19		Apparatus for scan testing printed circuit boards	324/754		324/754
3	X		US 2004020489 3 A1	20041014 25		Instruction register and access port gated clock for scan cells	702/117		702/117
4	X		US 2004019605 8 A1	20041007 14		Method for fabricating a probe pin for testing electrical characteristics of an apparatus	324/754		324/754
5	X	X	US 2004009213 5 A1	20040513 20		Electronic assembly tester and method for optoelectronic device	439/39		439/39
6	X		US 2004002203 9 A1	20040205 6		Reduced inductance contactor	361/767	174/265; 324/754; 324/765; 361/769	324/754; 324/765
7	X		US 2003018943 8 A1	20031009 8		ELECTRICAL TEST PROBE WEDGE TIP	324/754		324/754

	Inventor	S	C	P	2	3	4	5	Image Doc. Displayed	PT
1	Campbell, Julie A.							US	200500052193	
2	Swart, Mark A.							US	200500001640	
3	Whetsel, Lee D. JR.							US	20040204893	
4	Matsunaga, Noriaki et al.							US	20040196058	
5	Hofmeister, Rudolf J. et al.							US	20040092135	X
6	Ortiz, Alfred E. et al.							US	20040022039	
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8	X		US 2003018943 7 A1	20031009	8	Notched electrical test probe tip	324/754		324/754
9	X		US 2003009496 1 A1	20030522	20	Apparatus for scan testing printed circuit boards	324/754		324/754
10	X		US 2002018047 1 A1	20021205	13	Signal launch connecting techniques	324/758		324/758
11	X		US 2002012590 9 A1	20020912	18	Testing of BGA and other CSP packages using probing techniques	324/765	324/754	324/754; 324/765
12	X		US 2002012590 8 A1	20020912	17	Testing of BGA and other CSP packages using probing techniques	324/765		324/765
13	X		US 2002010951 9 A1	20020815	30	Planar subassembly for testing IC chips having faces with pressed electrical contacts that carry all power and signals for the chips	324/765		324/765

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8	Campbell, Julie A.						US	20030189437		
9	Swart, Mark A.						US	20030094961		
10	Cartier, Marc						US	20020180471		
11	Mehtha, Rajiv et al.						US	20020125909		
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14	X	US 0 A1	2002009336	20020718	21	Integrate circuit device	324 / 765		324 / 765	
15	X	US 3 A1	2002004398	20020418	14	Chip-testing socket using surface mount technology	324 / 755		324 / 755	

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14	Nagata, Junichi								US 20020093360	
15	Cheng, Wei-Jen								US 20020043983	